Notice of Allowability	Application No.	Applicant(s)
	10/697,816	CHANG ET AL.
	Examiner	Art Unit
	Ramesh B. Patel	2121
The MAILING DATE of this communication apperation apperation allowable, PROSECUTION ON THE MERITS IS therewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI	(OR REMAINS) CLOSED in or other appropriate communi GHTS. This application is su	this application. If not included nication will be mailed in due course. THIS
1. X This communication is responsive to 10/29/2003.		
2. 🔀 The allowed claim(s) is/are <u>1-5</u> .		
3. ⊠ The drawings filed on <u>29 <i>October 2003</i></u> are accepted by the Examiner.		
A. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  a)		
Attachment(s)  1. ☑ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 10/29/2003  4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Su Paper No./N 8), 7. ☐ Examiner's A	ormal Patent Application (PTO-152) mmary (PTO-413), Mail Date Amendment/Comment Statement of Reasons for Allowance
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## **DETAILED ACTION**

1. Claims 1-5 are allowed.

## **Priority**

Acknowledgment is made of applicant's claim for foreign priority under 35
 U.S.C. 119(a)-(d). The certified copy has been received and filed in the application.

## **Information Disclosure Statement**

- 3. The information disclosure statement (IDS) submitted on 10/29/2003 is in compliance with the provisions of 37 CFR 1.97. Accordingly, the information disclosure statement considered by the examiner.
- 4. The following is an examiner's statement of reasons for allowance: the prior art of the record teaches the method and the system for automatically tuning a PID controller in PID control loop; however, the prior art of the record particularly fails to teach or fairly suggest in combination with the other elements and features of the claimed invention regarding claims 1-5, a method for tuning a PID controller, the method comprising the steps of: inducing equivalent relationships between PID gains of the PID controller and parameters of time delay control (TDC); selecting a natural frequency vector and a damping ratio vector so as to acquire a desired error dynamics of the closed PID control loop system; determining the parameters of TDC on the basis of the natural frequency

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vector, the damping ratio vector and a closed loop stability condition for TDC; and selecting PID gains of the PID controller on the basis of the equivalent relationships.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ramesh B. Patel whose telephone number is 571-272-3688. The examiner can normally be reached on M-Th; 7:00 AM to 5:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Anthony Knight can be reached on 571-272-3687. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Ramesn B. Patel Primary Examiner

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